

Position

Professor, Department of Information and Computer Science, Faculty of Science and Technology, Keio University

Education

Keio University	Tokyo, Japan	Doctor of Engineering	1991
Keio University	Tokyo, Japan	Master of Engineering	1983
Keio University	Tokyo, Japan	Bachelor of Engineering	1981

Research area

Next Generation photonic IP network, protocol and systems, IP/MPLS integration, GMPLS, Network Security, photonic device

Professional History

- 2004- Present Keio University, Faculty of Science and Technology, Department of Information and Computer Science, Professor
- 2001- 2004 NTT Network Innovation Laboratories, Senior Research Engineer, Supervisor, Research Group Leader, (Photonic network architecture, Next generation IP photonic router, GMPLS)
- 1999 - 2001 NTT Network Service Systems Laboratories, Senior Research Engineer, Supervisor, Research Group Leader, (Broadband Switching system, R&D High-reliability backbone MPLS router “Type-X”)
- 1996 - 1999 NTT Network Service Systems Laboratories, Senior Research Scientist, Distinguished Technical Member, Supervisor, (Future communication network architecture, Future switching system R&D)
- 1992 - 1996 NTT Network Service Systems Laboratories, Senior Research Engineer (Broadband network backbone node system R&D)
- 1990 - 1992 NTT Transmission Systems Laboratories, Senior Research Engineer (Traffic control ATM Transmission System R&D)
- 1983 - 1990 NTT Musashino Laboratory, Engineer (High-speed Switching System, MCM, Optical Inter Connection and High-speed Switching VLSI R&D)
- 1982 - 1983 Master degree, Graduate school of Engineering, Instrumentation Engineer, Keio University, supervised by Prof. Yujiro Oba.
- 1980 - 1981 Bachelor degree, School of Instrumentation Engineer, Keio University, supervised by Prof. Yujiro Oba: Study on neutron measurement and it application on the cyclotron